MSC-CS CRYPTOGRAPHY & NETWORK SECURITY

UNIT - 3

Unit-3: Advanced Encryption Standard:

Finite Field Arithmetic – AES Structure – AES

Transformation Functions – AES Key Expansion – An

AES Example – AES Implementation

Public Key Cryptography and RSA:

Principles of Public Key Cryptosystems – The RSA

Algorithm

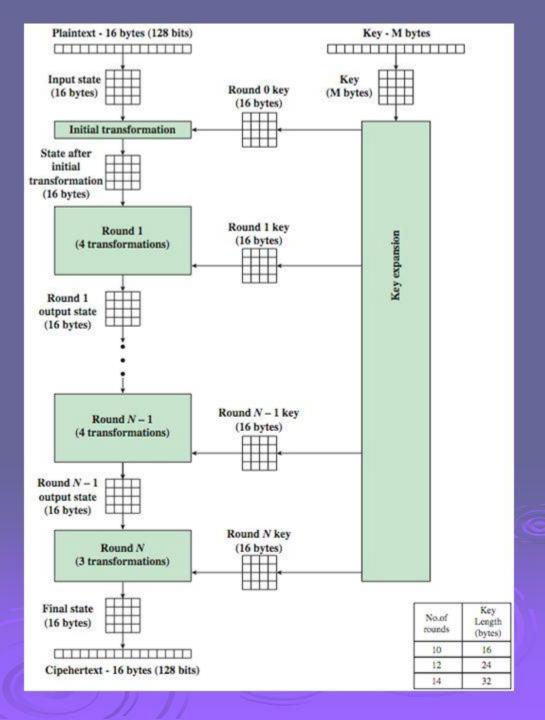
Origins

- clear a replacement for DES was needed
 - have theoretical attacks that can break it
 - have demonstrated exhaustive key search attacks
- can use Triple-DES but slow, has small blocks
- US NIST issued call for ciphers in 1997
- 15 candidates accepted in Jun 98
- 5 were shortlisted in Aug-99
- Rijndael was selected as the AES in Oct-2000
- issued as FIPS PUB 197 standard in Nov-2001

The AES Cipher - Rijndael

- designed by Rijmen-Daemen in Belgium
- has 128/192/256 bit keys, 128 bit data
- an iterative rather than feistel cipher
 - processes data as block of 4 columns of 4 bytes
 - operates on entire data block in every round
- designed to be:
 - resistant against known attacks
 - speed and code compactness on many CPUs
 - design simplicity

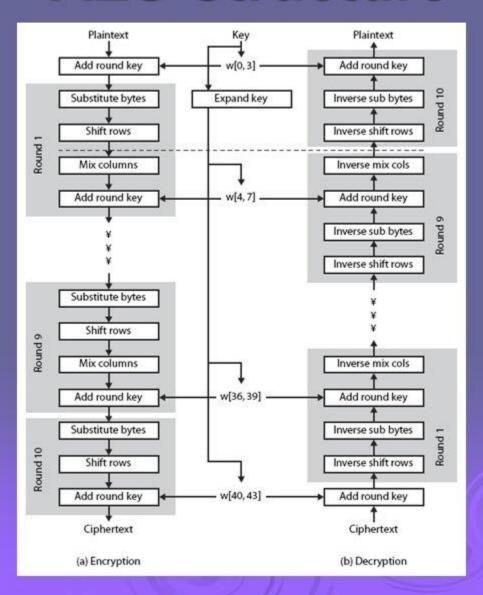
AES Encryption Process



AES Structure

- data block of 4 columns of 4 bytes is state
- key is expanded to array of words
- has 9/11/13 rounds in which state undergoes:
 - byte substitution (1 S-box used on every byte)
 - shift rows (permute bytes between groups/columns)
 - mix columns (subs using matrix multiply of groups)
 - add round key (XOR state with key material)
 - view as alternating XOR key & scramble data bytes
- initial XOR key material & incomplete last round
- with fast XOR & table lookup implementation

AES Structure



Some Comments on AES

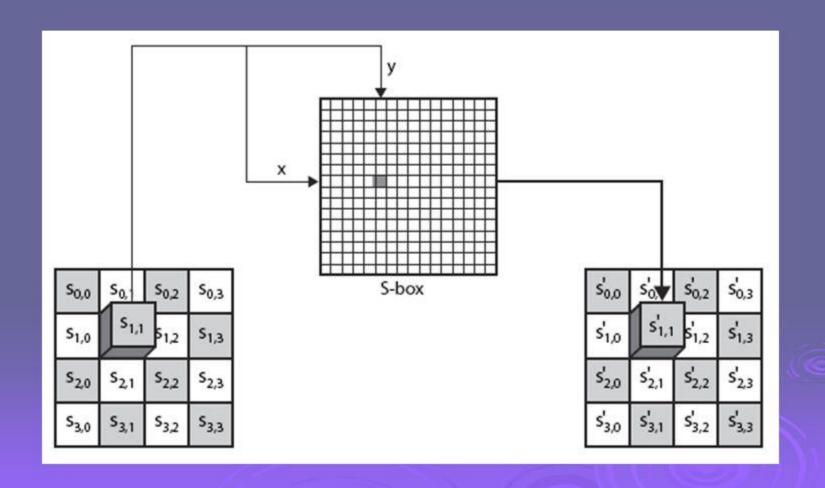
- an iterative rather than feistel cipher
- 2. key expanded into array of 32-bit words
 - four words form round key in each round
- 4 different stages are used as shown
- has a simple structure
- only AddRoundKey uses key
- AddRoundKey a form of Vernam cipher
- each stage is easily reversible
- decryption uses keys in reverse order
- decryption does recover plaintext
- 10. final round has only 3 stages

AES Transformation Functions

Substitute Bytes

- a simple substitution of each byte
- uses one table of 16x16 bytes containing a permutation of all 256 8-bit values
- each byte of state is replaced by byte indexed by row (left 4-bits) & column (right 4-bits)
 - eg. byte {95} is replaced by byte in row 9 column 5
 - which has value {2A}
- S-box constructed using defined transformation of values in GF(2⁸)
- designed to be resistant to all known attacks

Substitute Bytes



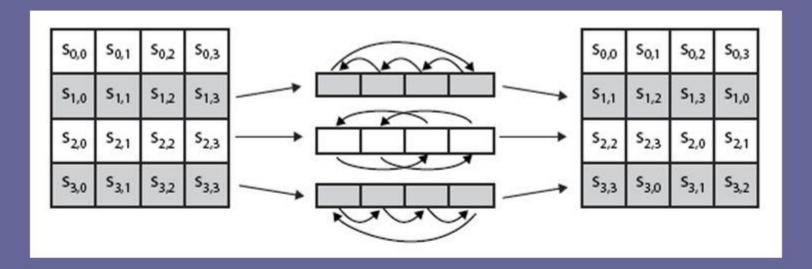
Substitute Bytes Example

EA	04	65	85		87	F2	4D	97
83	45	5D	96	_	EC	6E	4C	90
5C	33	98	B0	_	4A	C3	46	E7
F0	2D	AD	C5		8C	D8	95	A6

Shift Rows

- a circular byte shift in each each
 - 1st row is unchanged
 - 2nd row does 1 byte circular shift to left
 - 3rd row does 2 byte circular shift to left
 - 4th row does 3 byte circular shift to left
- decrypt inverts using shifts to right
- since state is processed by columns, this step permutes bytes between the columns

Shift Rows



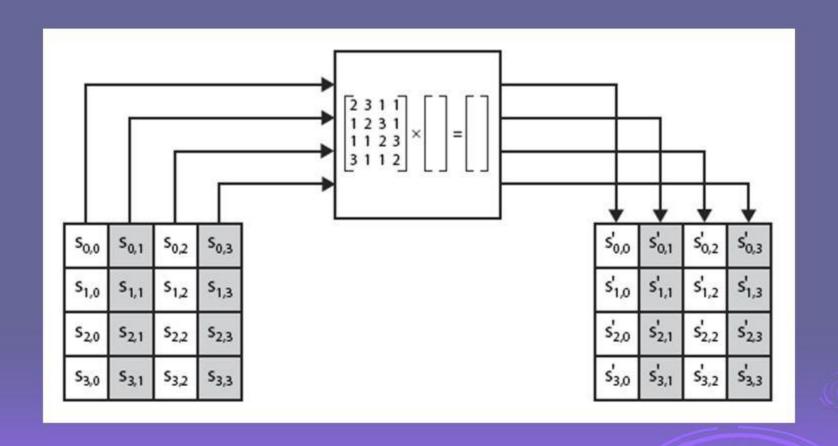
87	F2	4D	97	87	F2	4D	97
EC	6E	4C	90	 6E	4C	90	EC
4A	C3	46	E7	46	E7	4A	C3
8C	D8	95	A6	A6	8C	D8	95

Mix Columns

- each column is processed separately
- each byte is replaced by a value dependent on all 4 bytes in the column
- effectively a matrix multiplication in GF(28) using prime poly m(x) =x8+x4+x3+x+1

$$\begin{bmatrix} 02 & 03 & 01 & 01 \\ 01 & 02 & 03 & 01 \\ 01 & 01 & 02 & 03 \\ 03 & 01 & 01 & 02 \end{bmatrix} \begin{bmatrix} s_{0,0} & s_{0,1} & s_{0,2} & s_{0,3} \\ s_{1,0} & s_{1,1} & s_{1,2} & s_{1,3} \\ s_{2,0} & s_{2,1} & s_{2,2} & s_{2,3} \\ s_{3,0} & s_{3,1} & s_{3,2} & s_{3,3} \end{bmatrix} = \begin{bmatrix} s_{0,0} & s_{0,1} & s_{0,2} & s_{0,3} \\ s_{1,0} & s_{1,1} & s_{1,2} & s_{1,3} \\ s_{2,0} & s_{2,1} & s_{2,2} & s_{2,3} \\ s_{3,0} & s_{3,1} & s_{3,2} & s_{3,3} \end{bmatrix}$$

Mix Columns



Mix Columns Example

87	F2	4D	97		47	40	A3	4C
6E	4C	90	EC	_	37	D4	70	9F
46	E7	4A	C3		94	E4	3A	42
A6	8C	D8	95		ED	A5	A6	BC

AES Arithmetic

- uses arithmetic in the finite field GF(28)
- with irreducible polynomial

```
m(x) = x^8 + x^4 + x^3 + x + 1
which is (100011011) or {11b}
```

e.g.
 {02} • {87} mod {11b} = (1 0000 1110) mod {11b}
 = (1 0000 1110) xor (1 0001 1011) = (0001 0101)

Mix Columns

- can express each col as 4 equations
 - to derive each new byte in col
- decryption requires use of inverse matrix
 - with larger coefficients, hence a little harder
- have an alternate characterisation
 - each column a 4-term polynomial
 - with coefficients in GF(2⁸)
 - and polynomials multiplied modulo (x⁴+1)
- coefficients based on linear code with maximal distance between codewords

Add Round Key

- XOR state with 128-bits of the round key
- again processed by column (though effectively a series of byte operations)
- inverse for decryption identical
 - since XOR own inverse, with reversed keys
- designed to be as simple as possible
 - a form of Vernam cipher on expanded key
 - requires other stages for complexity / security

Add Round Key

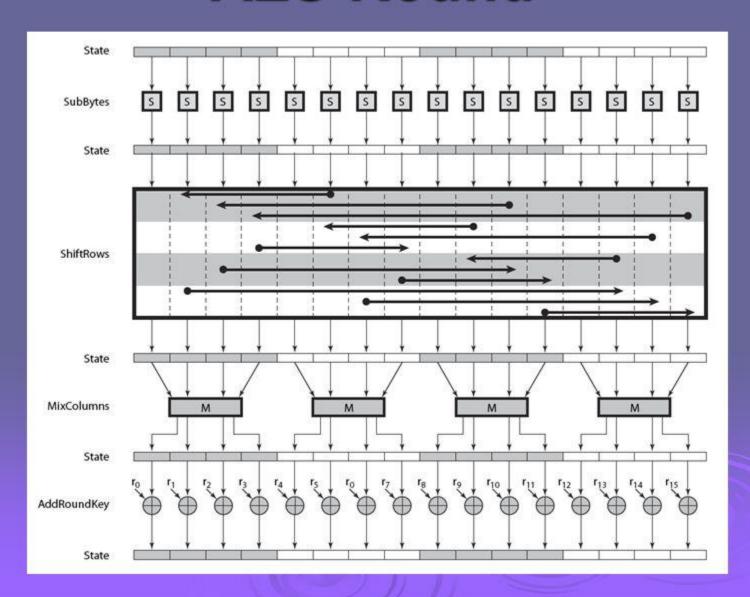
S _{0,0}	S _{0,1}	S _{0,2}	S _{0,3}
S _{1,0}	S _{1,1}	s _{1,2}	S _{1,3}
S _{2,0}	S _{2,1}	S _{2,2}	S _{2,3}
S _{3,0}	S _{3,1}	S _{3,2}	S _{3,3}

$$\oplus$$

Wi	W _{i+1}	W _{i+2}	W _{i+3}
----	------------------	------------------	------------------

s' _{0,0}	s' _{0,1}	s' _{0,2}	s' _{0,3}
s' _{1,0}	s' _{1,1}	s' _{1,2}	s' _{1,3}
s' _{2,0}	s' _{2,1}	s' _{2,2}	s' _{2,3}
s' _{3,0}	s' _{3,1}	s' _{3,2}	s' _{3,3}

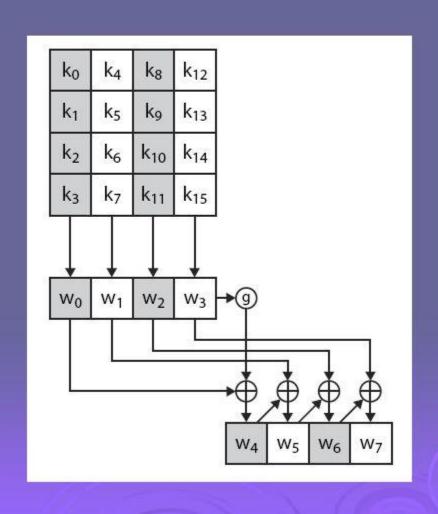
AES Round



AES Key Expansion

- takes 128-bit (16-byte) key and expands into array of 44/52/60 32-bit words
- start by copying key into first 4 words
- then loop creating words that depend on values in previous & 4 places back
 - in 3 of 4 cases just XOR these together
 - 1st word in 4 has rotate + S-box + XOR round constant on previous, before XOR 4th back

AES Key Expansion



Key Expansion Rationale

- designed to resist known attacks
- design criteria included
 - knowing part key insufficient to find many more
 - invertible transformation
 - fast on wide range of CPU's
 - use round constants to break symmetry
 - diffuse key bits into round keys
 - enough non-linearity to hinder analysis
 - simplicity of description

AES Example Key Expansion

Key Words	Auxiliary Function
w0 = 0f 15 71 c9	RotWord(w3)= 7f 67 98 af = x1
w1 = 47 d9 e8 59	SubWord(x1)= d2 85 46 79 = y1
w2 = 0c b7 ad	Rcon(1)= 01 00 00 00
w3 = af 7f 67 98	y1 ⊕ Rcon(1)= d3 85 46 79 = z1
w4 = w0 ⊕ z1 = dc 90 37 b0	RotWord(w7)= 81 15 a7 38 = x2
w5 = w4 ⊕ w1 = 9b 49 df e9	SubWord(x4)= 0c 59 5c 07 = y2
w6 = w5 ⊕ w2 = 97 fe 72 3f	Rcon(2)= 02 00 00 00
w7 = w6 ⊕ w3 = 38 81 15 a7	y2 ⊕ Rcon(2)= 0e 59 5c 07 = z2
w8 = w4 ⊕ z2 = d2 c9 6b b7	RotWord(wl1) = ff d3 c6 e6 = x3
w9 = w8 ⊕ w5 = 49 80 b4 5e	SubWord(x2)= 16 66 b4 8e = y3
w10 = w9 ⊕ w6 = de 7e c6 61	Rcon(3)= 04 00 00 00
wl1 = w10 ⊕ w7 = e6 ff d3 c6	y3 ① Rcon(3)= 12 66 b4 8e = z3
w12 = w8 ⊕ z3 = c0 af df 39	RotWord(w15) = ae 7e c0 b1 = x4
w13 = w12 + w9 = 89 2f 6b 67	SubWord(x3)= e4 f3 ba c8 = y4
w14 = w13 ⊕ w10 = 57 51 ad 06	Rcon(4)= 08 00 00 00
w15 = w14 @ w11 = b1 ae 7e c0	y4 ① Rcon(4)= ec f3 ba c8 = 4
w16 = w12 ⊕ z4 = 2c 5c 65 f1	RotWord(w19) = 8c dd 50 43 = x5
w17 = w16 ⊕ w13 = a5 73 0e 96	SubWord(x4)= 64 cl 53 la = y5
w18 = w17 \oplus w14 = f2 22 a3 90	Rcon(5)= 10 00 00 00
w19 = w18 (+) w15 = 43 8c dd 50	y5 ⊕ Rcon(5)= 74 cl 53 la = z5
w20 = w16 ⊕ z5 = 58 9d 36 eb	RotWord(w23) = 40 46 bd 4c = x6
w21 = w20 ⊕ w17 = fd ee 38 7d	SubWord(x5)= 09 5a 7a 29 = y6
w22 = w21 ⊕ w18 = 0f cc 9b ed	Rcon(6) = 20 00 00 00
w23 = w22	y6 ⊕ Rcon(6)= 29 5a 7a 29 = z6
w24 = w20 ⊕ z6 = 71 c7 4c c2	RotWord($w27$) = a5 a9 ef cf = $x7$
w25 = w24 ⊕ w21 = 8c 29 74 bf	SubWord(x6)= 06 d3 df 8a = y7
w26 = w25 ⊕ w22 = 83 e5 ef 52	Rcon(7)= 40 00 00 00
w27 = w26 ⊕ w23 = cf a5 a9 ef	y7 ⊕ Rcon(7)= 46 d3 df 8a = z7
w28 = w24 ⊕ z7 = 37 14 93 48	RotWord(w31) = 7d al 4a f7 = x8
w29 = w28 ⊕ w25 = bb 3d e7 f7	SubWord(x7)= ff 32 d6 68 = y8
w30 = w29 ⊕ w26 = 38 d8 08 a5	Rcon(8) = 80 00 00 00 y8 ** Rcon(8) = 7f 32 d6 68 = z8
w31 = w30 ⊕ w27 = f7 7d al 4a	
w32 = w28 ⊕ z8 = 48 26 45 20 w33 = w32 ⊕ w29 = f3 1b a2 d7	RotWord(w35)= be 0b 38 3c = x9 SubWord(x8)= ae 2b 07 eb = y9
w34 = w33 ⊕ w30 = cb c3 aa 72	Rcon(9) = 1B 00 00 00
w34 - w33 ⊕ w30 - cb c3 aa 72 w35 = w34 ⊕ w32 = 3c be 0b 38	y9 ⊕ Rcon(9)= b5 2b 07 eb = z9
w36 = w32 ⊕ z9 = fd 0d 42 cb	RotWord(w39) = 6b 41 56 f9 = x10
w30 = w32 ⊕ 29 = 1d od 42 cb w37 = w36 ⊕ w33 = 0e 16 e0 1c	SubWord(x9)= 7f 83 b1 99 = y10
w37 = w37 ⊕ w34 = c5 d5 4a 6e	Rcon(10)= 36 00 00 00
w39 = w38 @ w35 = f9 6b 41 56	y10 ⊕ Rcon(10)= 49 83 b1 99 = z10
w40 = w36 ⊕ z10 = b4 8e f3 52	
w41 = w40 ⊕ w37 = ba 98 13 4e	
w42 = w41 ⊕ w38 = 7f 4d 59 20	
-1212 0 -20 - 20 20 10 70	

w43 = w42 @ w39 = 86 26 18 76

AES Example Encryption

Start of round	After Sub-Puter	After ShiftRows	After MixColumns	Round Key
01 89 fe 76 23 ab dc 54 45 cd ba 32 67 ef 98 10	SubBytes	SantRows	MIXCOLUMNS	0f 47 0c af 15 d9 b7 7f 71 e8 ad 67 c9 59 d6 98
67 ef 98 10 0e ce f2 d9 36 72 6b 2b 34 25 17 55 ae b6 4e 88	ab 8b 89 35 05 40 7f fl 18 3f f0 fc e4 4e 2f c4	ab 8b 89 35 40 7f fl 05 f0 fc 18 3f c4 e4 4e 2f	b9 94 57 75 e4 8e 16 51 47 20 9a 3f c5 d6 f5 3b	dc 9b 97 38 90 49 fe 81 37 df 72 15 b0 e9 3f a7
65 Of c0 4d	4d 76 ba e3	4d 76 ba e3	8e 22 db 12	d2 49 de e6
74 c7 e8 d0	92 c6 9b 70	c6 9b 70 92	b2 f2 dc 92	c9 80 7e ff
70 ff e8 2a	51 16 9b e5	9b e5 51 16	df 80 f7 c1	6b b4 c6 d3
75 3f ca 9c	9d 75 74 de	de 9d 75 74	2d c5 le 52	b7 5e 61 c6
5c 6b 05 f4	4a 7f 6b bf	4a 7f 6b bf	b1 c1 0b cc	c0 89 57 bl
7b 72 a2 6d	21 40 3a 3c	40 3a 3c 21	ba f3 8b 07	af 2f 51 ae
b4 34 31 12	8d 18 c7 c9	c7 c9 8d 18	f9 1f 6a c3	df 6b ad 7e
9a 9b 7f 94	b8 14 d2 22	22 b8 14 d2	ld 19 24 5c	39 67 06 c0
71 48 5c 7d	a3 52 4a ff	a3 52 4a ff	d4 11 fe 0f	2c a5 f2 43
15 dc da a9	59 86 57 d3	86 57 d3 59	3b 44 06 73	5c 73 22 8c
26 74 c7 bd	f7 92 c6 7a	c6 7a f7 92	cb ab 62 37	65 0e a3 dd
24 7e 22 9c	36 f3 93 de	de 36 f3 93	19 b7 07 ec	f1 96 90 50
f8 b4 0c 4c	41 8d fe 29	41 8d fe 29	2a 47 c4 48	58 fd 0f 4c
67 37 24 ff	85 9a 36 16	9a 36 16 85	83 e8 18 ba	9d ee cc 40
ae a5 c1 ea	e4 06 78 87	78 87 e4 06	84 18 27 23	36 38 9b 46
e8 21 97 bc	9b fd 88 65	65 9b fd 88	eb 10 0a f3	eb 7d ed bd
72 ba cb 04	40 f4 lf f2	40 f4 lf f2	7b 05 42 4a	71 8c 83 cf
1e 06 d4 fa	72 6f 48 2d	6f 48 2d 72	1e d0 20 40	c7 29 e5 a5
b2 20 bc 65	37 b7 65 4d	65 4d 37 b7	94 83 18 52	4c 74 ef a9
00 6d e7 4e	63 3c 94 2f	2f 63 3c 94	94 c4 43 fb	c2 bf 52 ef
0a 89 cl 85	67 a7 78 97	67 a7 78 97	ec la c0 80	37 bb 38 f7
d9 f9 c5 e5	35 99 a6 d9	99 a6 d9 35	0c 50 53 c7	14 3d d8 7d
d8 f7 f7 fb	61 68 68 0f	68 Of 61 68	3b d7 00 ef	93 e7 08 a1
56 7b 11 14	b1 21 82 fa	fa bl 21 82	b7 22 72 e0	48 f7 a5 4a
db a1 f8 77	b9 32 41 f5	b9 32 41 f5	b1 la 44 17	48 f3 cb 3c
18 6d 8b ba	ad 3c 3d f4	3c 3d f4 ad	3d 2f ec b6	26 lb c3 be
a8 30 08 4e	c2 04 30 2f	30 2f c2 04	0a 6b 2f 42	45 a2 aa 0b
ff d5 d7 aa f9 e9 8f 2b 1b 34 2f 08 4f c9 85 49	99 le 73 fl af 18 15 30 84 dd 97 3b	ac 16 03 0e 99 1e 73 fl 18 15 30 af 97 3b 84 dd	9f 68 f3 b1 31 30 3a c2 ac 71 8c c4 46 65 48 eb	20 d7 72 38 fd 0e c5 f9 0d 16 d5 6b 42 e0 4a 41
bf bf 81 89 cc 3e ff 3b a1 67 59 af 04 85 02 aa a1 00 5f 34	08 08 0c a7 4b b2 16 e2 32 85 cb 79 f2 97 77 ac	a7 08 08 0c 4b b2 16 e2 85 cb 79 32 77 ac f2 97	6a 1c 31 62 4b 86 8a 36 b1 cb 27 5a fb f2 f2 af	cb 1c 6e 56 b4 8e f3 52 ba 98 13 4e 7f 4d 59 20
ff 08 69 64 0b 53 34 14 84 bf ab 8f 4a 7c 43 b9	32 63 cf 18	18 32 63 cf	cc 5a 5b cf	86 26 18 76

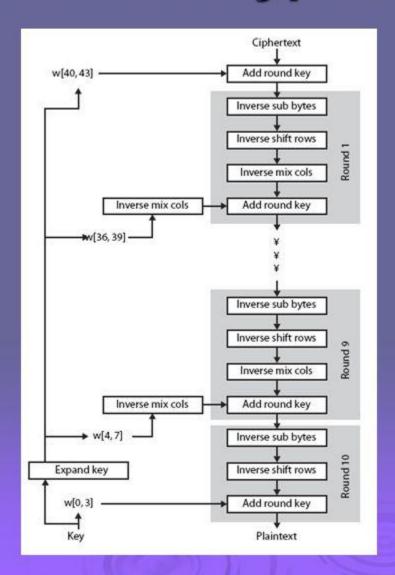
AES Example Avalanche

Round		Number of bits that differ
	0123456789abcdeffedcba9876543210	1
	0023456789abcdeffedcba9876543210	1
0	0e3634aece7225b6f26b174ed92b5588	1
U	0f3634aece7225b6f26b174ed92b5588	1
1	657470750fc7ff3fc0e8e8ca4dd02a9c	20
•	c4a9ad090fc7ff3fc0e8e8ca4dd02a9c	20
2	5c7bb49a6b72349b05a2317ff46d1294	58
2	fe2ae569f7ee8bb8c1f5a2bb37ef53d5	36
3	7115262448dc747e5cdac7227da9bd9c	59
3	ec093dfb7c45343d689017507d485e62	39
4	f867aee8b437a5210c24c1974cffeabc	61
7	43efdb697244df808e8d9364ee0ae6f5	01
5	721eb200ba06206dcbd4bce704fa654e	68
3	7b28a5d5ed643287e006c099bb375302	00
6	0ad9d85689f9f77bc1c5f71185e5fb14	64
U	3bc2d8b6798d8ac4fe36a1d891ac181a	04
7	db18a8ffa16d30d5f88b08d777ba4eaa	67
	9fb8b5452023c70280e5c4bb9e555a4b	07
8	f91b4fbfe934c9bf8f2f85812b084989	65
0	20264e1126b219aef7feb3f9b2d6de40	0.5
9	cca104a13e678500ff59025f3bafaa34	61
,	b56a0341b2290ba7dfdfbddcd8578205	01
10	ff0b844a0853bf7c6934ab4364148fb9	58
10	612b89398d0600cde116227ce72433f0	30

AES Decryption

- AES decryption is not identical to encryption since steps done in reverse
- but can define an equivalent inverse cipher with steps as for encryption
 - but using inverses of each step
 - with a different key schedule
- works since result is unchanged when
 - swap byte substitution & shift rows
 - swap mix columns & add (tweaked) round key

AES Decryption



Implementation Aspects

- can efficiently implement on 8-bit CPU
 - byte substitution works on bytes using a table of 256 entries
 - shift rows is simple byte shift
 - add round key works on byte XOR's
 - mix columns requires matrix multiply in GF(2⁸) which works on byte values, can be simplified to use table lookups & byte XOR's

Implementation Aspects

- can efficiently implement on 32-bit CPU
 - redefine steps to use 32-bit words
 - can precompute 4 tables of 256-words
 - then each column in each round can be computed using 4 table lookups + 4 XORs
 - at a cost of 4Kb to store tables
- designers believe this very efficient implementation was a key factor in its selection as the AES cipher

Private-Key Cryptography

- traditional private/secret/single key cryptography uses one key
- > shared by both sender and receiver
- ➤ if this key is disclosed communications are compromised
- > also is symmetric, parties are equal
- hence does not protect sender from receiver forging a message & claiming is sent by sender

Public-Key Cryptography

- probably most significant advance in the 3000 year history of cryptography
- uses two keys a public & a private key
- asymmetric since parties are not equal
- uses clever application of number theoretic concepts to function
- complements rather than replaces private key crypto

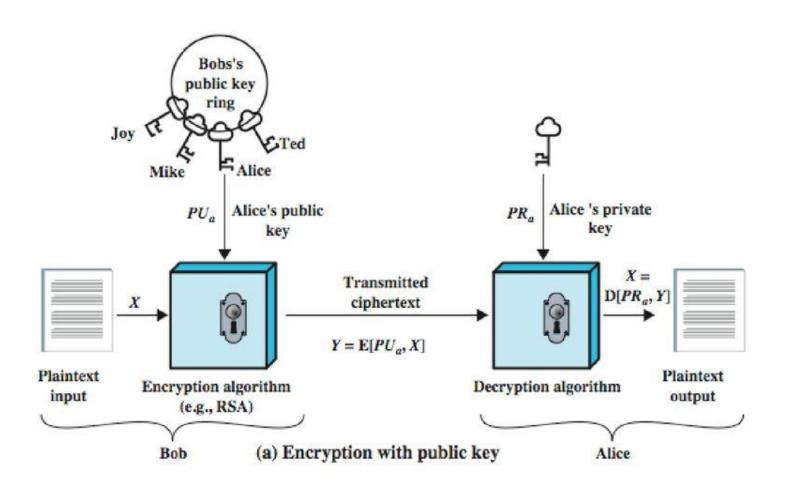
Why Public-Key Cryptography?

- developed to address two key issues:
 - key distribution how to have secure communications in general without having to trust a KDC with your key
 - digital signatures how to verify a message comes intact from the claimed sender
- public invention due to Whitfield Diffie & Martin Hellman at Stanford Uni in 1976
 - known earlier in classified community

Public-Key Cryptography

- public-key/two-key/asymmetric cryptography involves the use of two keys:
 - a public-key, which may be known by anybody, and can be used to encrypt messages, and verify signatures
 - a related private-key, known only to the recipient, used to decrypt messages, and sign (create) signatures
- infeasible to determine private key from public
- is asymmetric because
 - those who encrypt messages or verify signatures cannot decrypt messages or create signatures

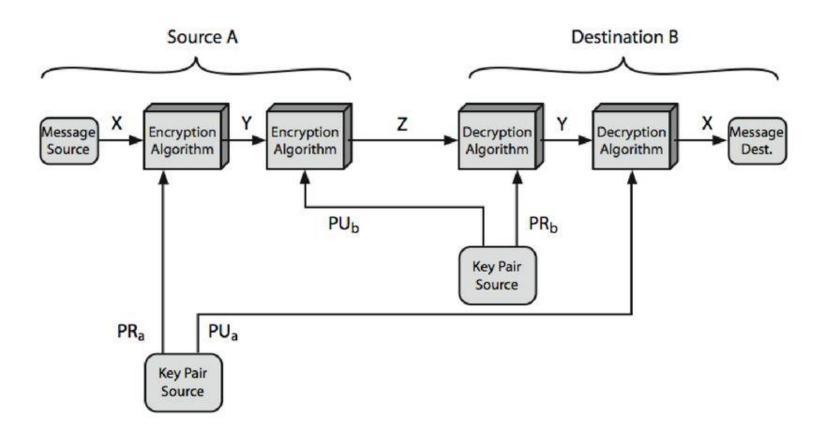
Public-Key Cryptography



Symmetric vs Public-Key

Conventional Encryption	Public-Key Encryption Needed to Work:		
Needed to Work:			
 The same algorithm with the same key is used for encryption and decryption. 	One algorithm is used for encryption and decryption with a pair of keys, one for encryption and one for decryption.		
The sender and receiver must share the algorithm and the key.	The sender and receiver must each have one of the matched pair of keys (not the content of		
Needed for Security:	same one).		
1. The key must be kept secret.	Needed for Security:		
It must be impossible or at least impractical to decipher a message if no	One of the two keys must be kept secret.		
other information is available.	It must be impossible or at least impractical to decipher a message if no		
 Knowledge of the algorithm plus samples of ciphertext must be 	other information is available.		
insufficient to determine the key.	 Knowledge of the algorithm plus one of the keys plus samples of ciphertext must be insufficient to determine the other key. 		

Public-Key Cryptosystems



Public-Key Applications

- can classify uses into 3 categories:
 - encryption/decryption (provide secrecy)
 - digital signatures (provide authentication)
 - key exchange (of session keys)
- some algorithms are suitable for all uses, others are specific to one

Algorithm	Encryption/Decryption	Digital Signature	Yes Yes Yes	
RSA	Yes	Yes		
Elliptic Curve	Yes	Yes		
Diffie-Hellman	No	No		
DSS	No	Yes	No	

Public-Key Requirements

- Public-Key algorithms rely on two keys where:
 - it is computationally infeasible to find decryption key knowing only algorithm & encryption key
 - it is computationally easy to en/decrypt messages when the relevant (en/decrypt) key is known
 - either of the two related keys can be used for encryption,
 with the other used for decryption (for some algorithms)
- these are formidable requirements which only a few algorithms have satisfied

Public-Key Requirements

- need a trapdoor one-way function
- one-way function has
 - Y = f(X) easy
 - $-X = f^{-1}(Y)$ infeasible
- a trap-door one-way function has
 - $Y = f_k(X)$ easy, if k and X are known
 - $X = f_k^{-1}(Y)$ easy, if k and Y are known
 - $X = f_k^{-1}(Y)$ infeasible, if Y known but k not known
- a practical public-key scheme depends on a suitable trap-door one-way function

Security of Public Key Schemes

- ▶ like private key schemes brute force exhaustive search attack is always theoretically possible
- ➤ but keys used are too large (>512bits)
- security relies on a large enough difference in difficulty between easy (en/decrypt) and hard (cryptanalyse) problems
- more generally the hard problem is known, but is made hard enough to be impractical to break
- requires the use of very large numbers
- hence is **slow** compared to private key schemes

RSA

- ➤ by Rivest, Shamir & Adleman of MIT in 1977
- best known & widely used public-key scheme
- based on exponentiation in a finite (Galois) field over integers modulo a prime
 - nb. exponentiation takes O((log n)³) operations (easy)
- uses large integers (eg. 1024 bits)
- security due to cost of factoring large numbers
 - nb. factorization takes O(e log n log log n) operations (hard)

RSA En/decryption

- to encrypt a message M the sender:
 - obtains public key of recipient PU={e,n}
 - computes: $C = M^e \mod n$, where $0 \le M \le n$
- to decrypt the ciphertext C the owner:
 - uses their private key PR={d,n}
 - computes: $M = C^d \mod n$
- note that the message M must be smaller than the modulus n (block if needed)

RSA Key Setup

- each user generates a public/private key pair by:
- selecting two large primes at random: p, q
- computing their system modulus n=p.q
 - note \emptyset (n) = (p-1) (q-1)
- selecting at random the encryption key e
 - where $1 < e < \emptyset(n)$, $gcd(e, \emptyset(n)) = 1$
- solve following equation to find decryption key d
 - -e.d=1 mod \emptyset (n) and 0≤d≤n
- publish their public encryption key: PU={e,n}
- keep secret private decryption key: PR={d,n}

Why RSA Works

- because of Euler's Theorem:
 - $a^{o(n)} \mod n = 1$ where gcd(a,n)=1
- in RSA have:
 - -n=p.q
 - $\emptyset(n) = (p-1)(q-1)$
 - carefully chose e & d to be inverses mod Ø(n)
 - hence $e.d=1+k.\varnothing(n)$ for some k
- hence:

$$C^{d} = M^{e \cdot d} = M^{1+k \cdot \emptyset(n)} = M^{1} \cdot (M^{\emptyset(n)})^{k}$$

= $M^{1} \cdot (1)^{k} = M^{1} = M \mod n$

RSA Example - Key Setup

- 1. Select primes: p=17 & q=11
- 2. Calculate $n = pq = 17 \times 11 = 187$
- 3. Calculate $\emptyset(n) = (p-1)(q-1) = 16x10 = 160$
- 4. Select e: gcd(e, 160) = 1; choose e=7
- 5. Determine d: $de=1 \mod 160$ and d < 160Value is d=23 since 23x7=161=10x160+1
- 6. Publish public key $PU = \{7, 187\}$
- 7. Keep secret private key $PR = \{23, 187\}$

RSA Example - En/Decryption

- ➤ sample RSA encryption/decryption is:
- \triangleright given message M = 88 (nb. 88<187)
- > encryption:

```
C = 88^7 \mod 187 = 11
```

> decryption:

```
M = 11^{23} \mod 187 = 88
```

Exponentiation

- can use the Square and Multiply Algorithm
- a fast, efficient algorithm for exponentiation
- concept is based on repeatedly squaring base
- and multiplying in the ones that are needed to compute the result
- look at binary representation of exponent
- only takes O(log₂ n) multiples for number n
 - $\text{ eg. } 7^5 = 7^4.7^1 = 3.7 = 10 \mod 11$
 - $\text{ eg. } 3^{129} = 3^{128}.3^1 = 5.3 = 4 \mod 11$

Exponentiation

```
c = 0; f = 1
for i = k \text{ downto } 0
     do c = 2 \times c
         f = (f \times f) \mod n
     if b_i == 1 then
         c = c + 1
         f = (f \times a) \mod n
return f
```

Efficient Encryption

- encryption uses exponentiation to power e
- hence if e small, this will be faster
 - often choose e=65537 (2¹⁶-1)
 - also see choices of e=3 or e=17
- but if e too small (eg e=3) can attack
 - using Chinese remainder theorem & 3 messages with different modulii
- if e fixed must ensure $gcd(e, \emptyset(n)) = 1$
 - ie reject any p or q not relatively prime to e

Efficient Decryption

- decryption uses exponentiation to power d
 - this is likely large, insecure if not
- can use the Chinese Remainder Theorem (CRT) to compute mod p & q separately. then combine to get desired answer
 - approx 4 times faster than doing directly
- only owner of private key who knows values of p & q can use this technique

RSA Key Generation

- users of RSA must:
 - determine two primes at random p, q
 - select either e or d and compute the other
- primes p, q must not be easily derived from modulus n=p.q
 - means must be sufficiently large
 - typically guess and use probabilistic test
- exponents e, d are inverses, so use Inverse algorithm to compute the other

RSA Security

- possible approaches to attacking RSA are:
 - brute force key search infeasible given size of numbers
 - mathematical attacks based on difficulty of computing ø(n), by factoring modulus n
 - timing attacks on running of decryption
 - chosen ciphertext attacks given properties of RSA

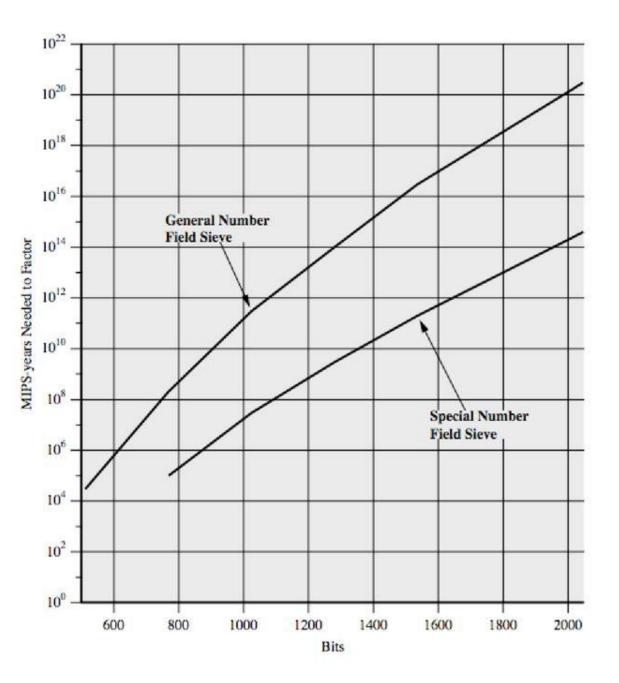
Factoring Problem

- mathematical approach takes 3 forms:
 - factor n=p.q, hence compute Ø(n) and then d
 - determine Ø (n) directly and compute d
 - find d directly
- currently believe all equivalent to factoring
 - have seen slow improvements over the years
 - as of May-05 best is 200 decimal digits (663) bit with LS
 - biggest improvement comes from improved algorithm
 - cf QS to GHFS to LS
 - currently assume 1024-2048 bit RSA is secure
 - ensure p, q of similar size and matching other constraints

Progress in Factoring

Number of Decimal Digits	Approximate Number of Bits	Date Achieved	MIPS-years	Algorithm
100	332	April 1991	7	quadratic sieve
110	365	April 1992	75	quadratic sieve
120	398	June 1993	830	quadratic sieve
129	428	April 1994	5000	quadratic sieve
130	431	April 1996	1000	generalized number field sieve
140	465	February 1999	2000	generalized number field sieve
155	512	August 1999	8000	generalized number field sieve
160	530	April 2003	e 	Lattice sieve
174	576	December 2003	<u></u>	Lattice sieve
200	663	May 2005	-	Lattice sieve

Progress in Factoring



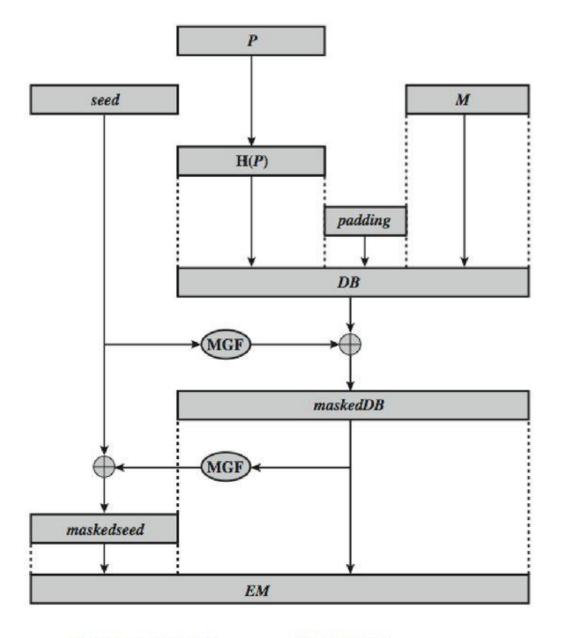
Timing Attacks

- developed by Paul Kocher in mid-1990's
- exploit timing variations in operations
 - eg. multiplying by small vs large number
 - or IF's varying which instructions executed
- infer operand size based on time taken
- RSA exploits time taken in exponentiation
- countermeasures
 - use constant exponentiation time
 - add random delays
 - blind values used in calculations

Chosen Ciphertext Attacks

RSA is vulnerable to a Chosen Ciphertext Attack (CCA) attackers chooses ciphertexts & gets decrypted plaintext back choose ciphertext to exploit properties of RSA to provide info to help cryptanalysis can counter with random pad of plaintext or use Optimal Asymmetric Encryption Padding (OASP)

Optimal
Asymmetric
Encryption
Padding
(OASP)



P = encoding parameters M = message to be encoded H = hash function DB = data block MGF = mask generating function EM = encoded message